



Fee only
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yi Ding
Assignee: ProMOS Technologies, Inc.
Title: Arrays of Nonvolatile Memory Cells Wherein Each Cell Has Two Conductive Floating Gates
Application No.: 10/632,007 Filing Date: July 30, 2003
Examiner: Ida M. Soward Group Art Unit: 2822
Docket No.: M-15223 US

San Jose, California
November 2, 2004

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.56

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are not enclosed.

Citation of these documents shall not be construed as:

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Michael Shenker 11-2-04
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Date of Signature

Respectfully submitted,

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